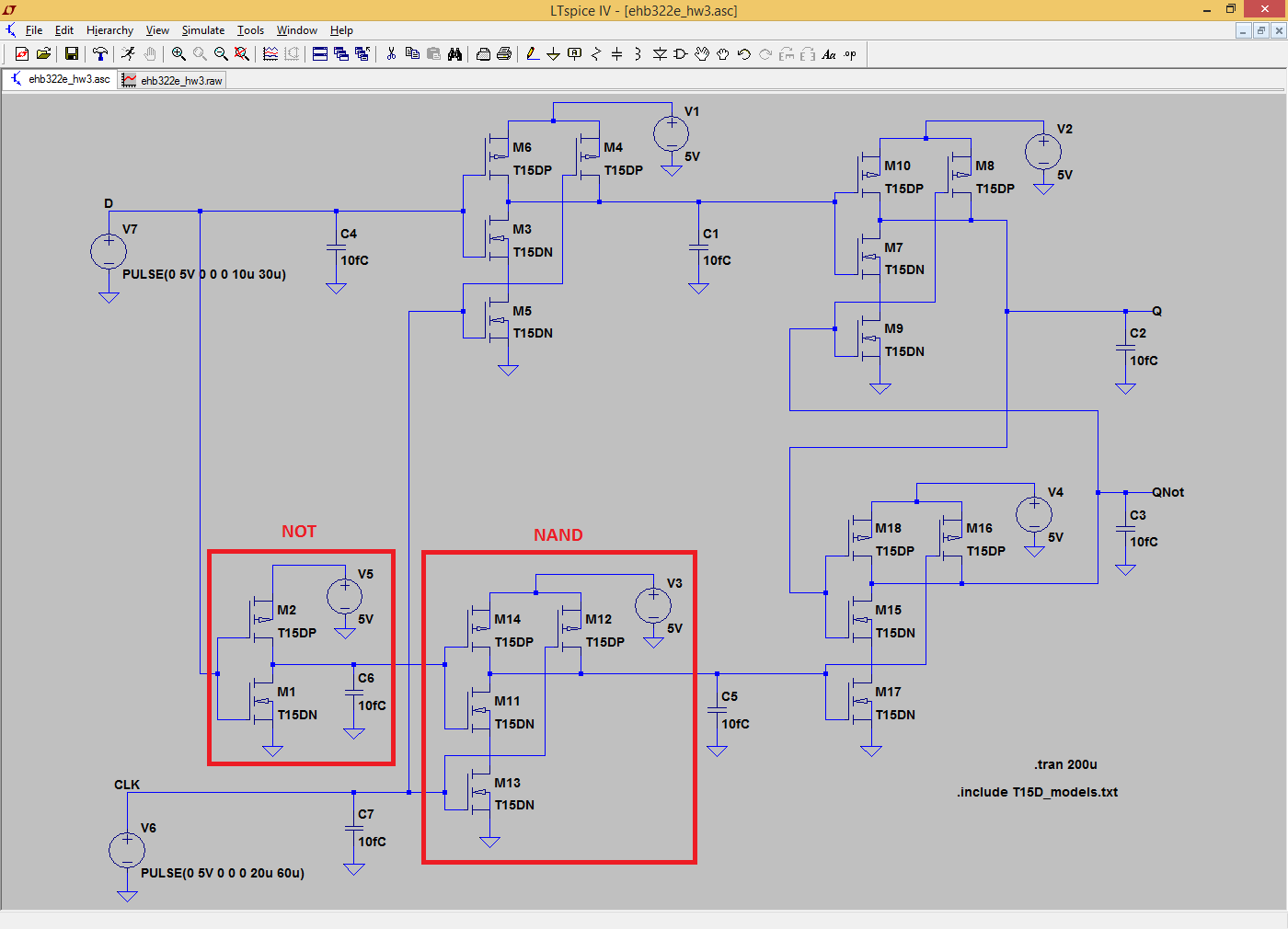
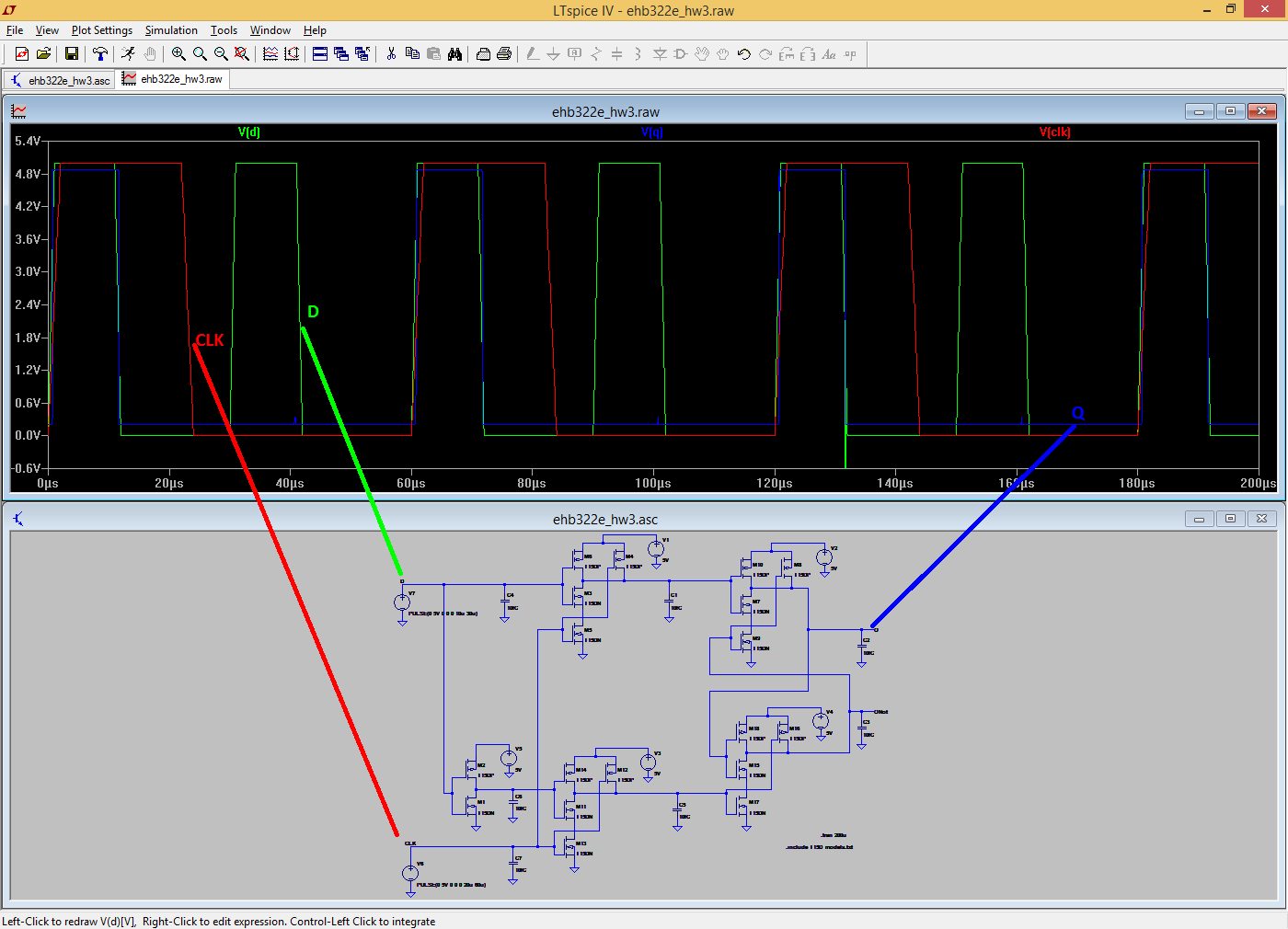
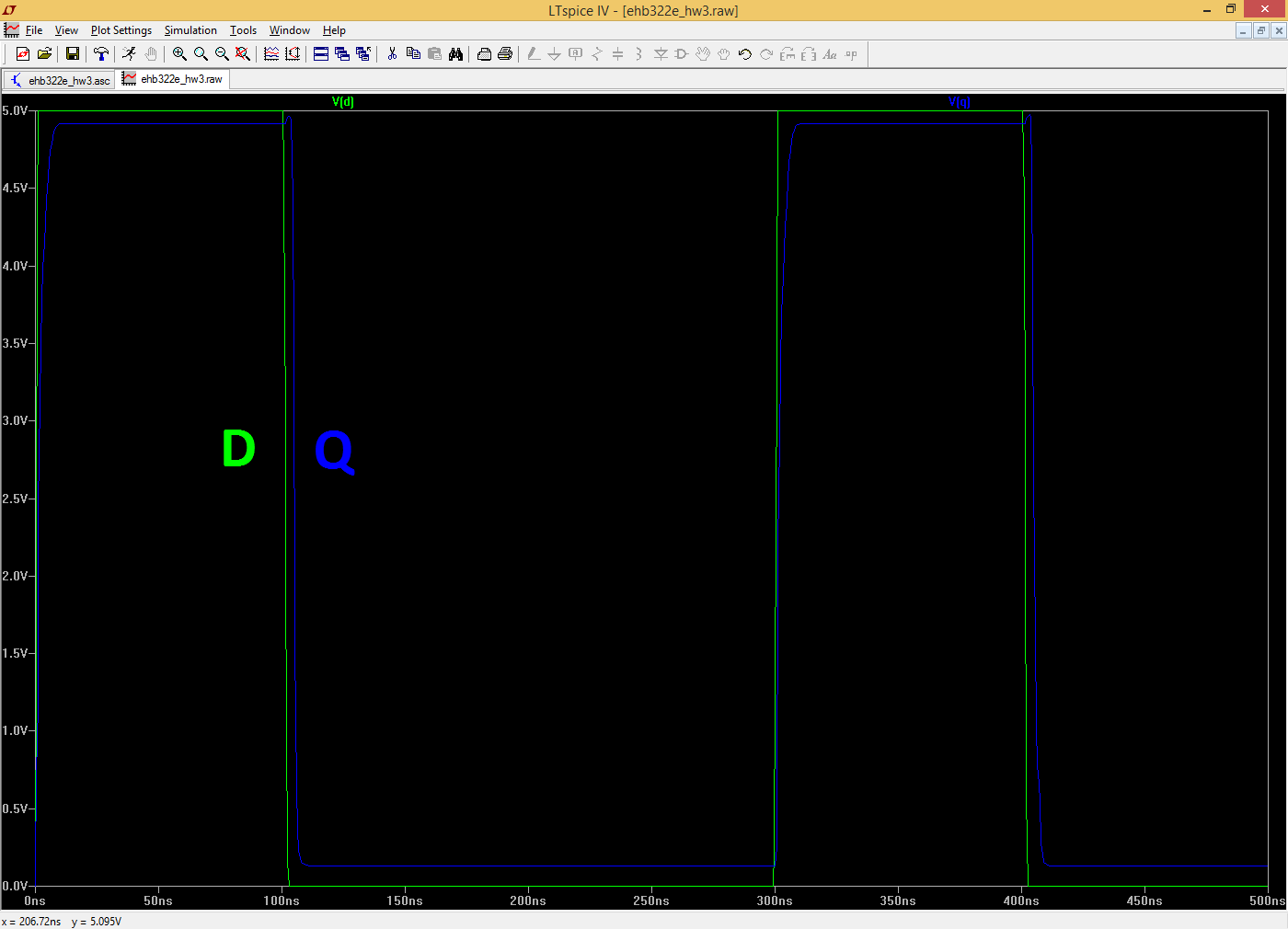
**CMOS D-flip-flop**

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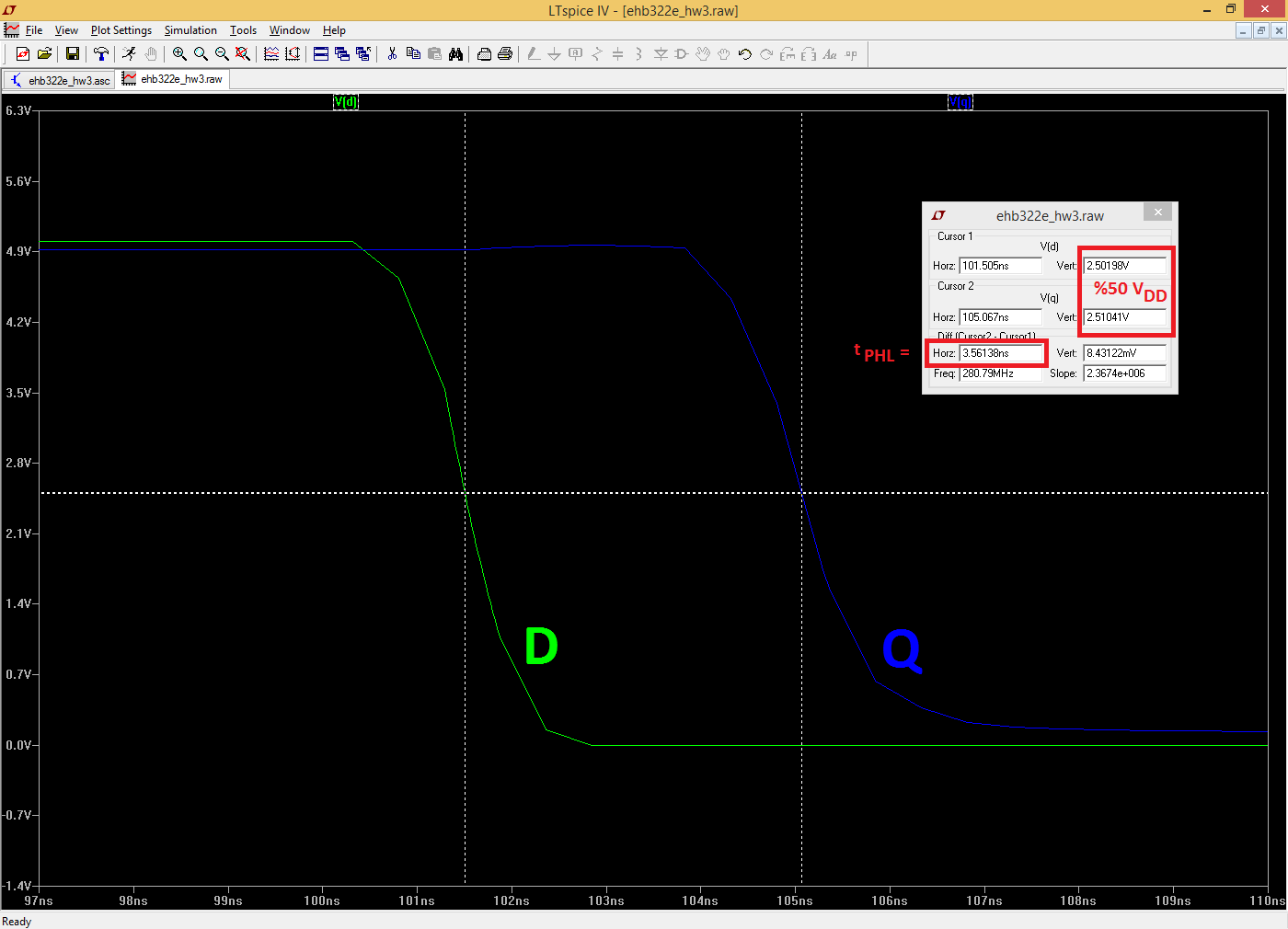
**Q output with D and CLK**

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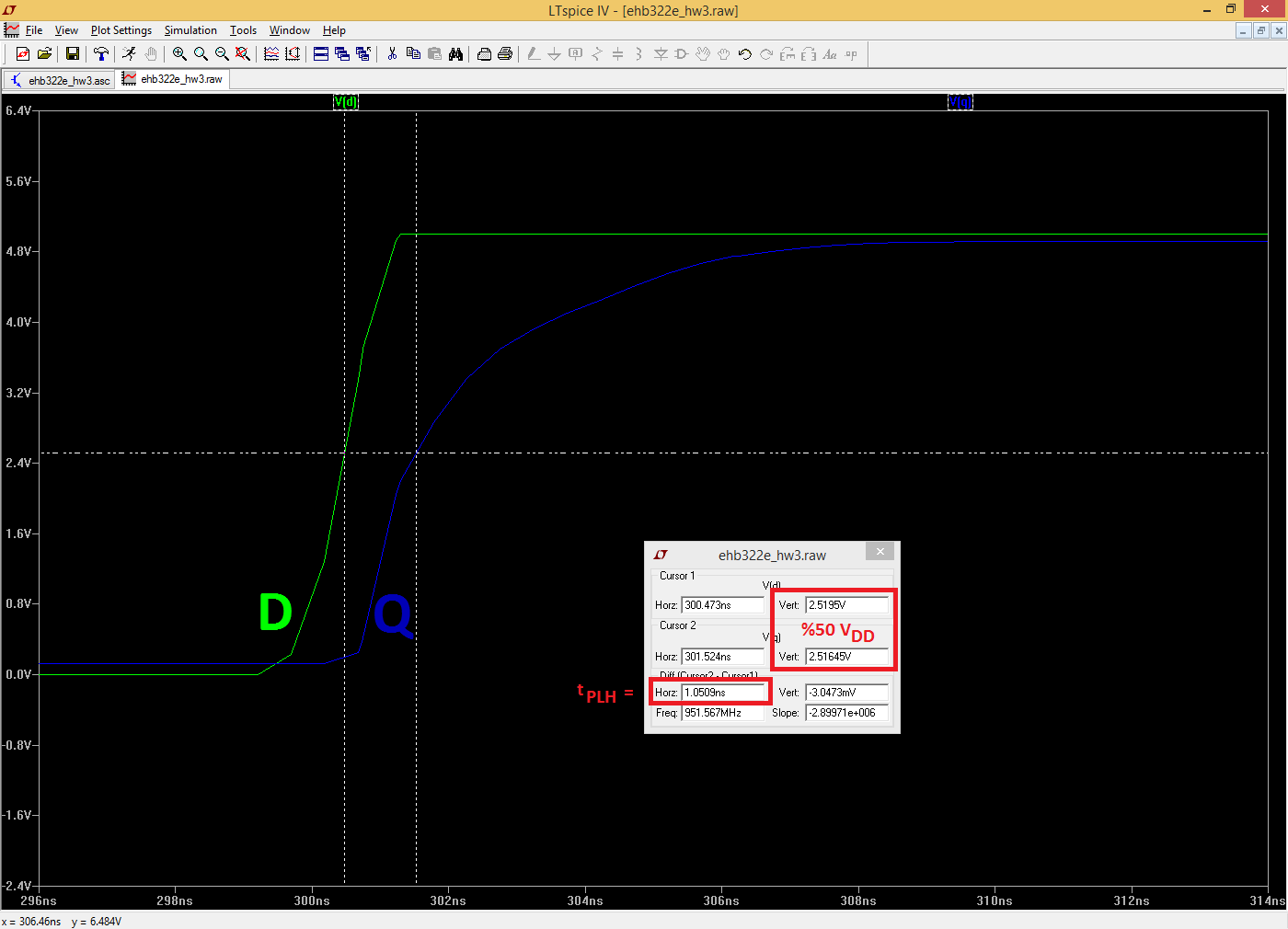
**W.C. PD at Q with D input**

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**W.C. Total tPHL at Q with D input**

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**W.C. Toltal tPLH at Q with D input**

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